

L Number	Hits	Search Text	DB	Time stamp
1	92	(((((711/5,101,104,105,154,165,170,173).CCLS. (712/2,6,10,11,14,15).CCLS.)) and @ad < "20000831") and (parallel adj processor)) and serial) and ((one single "1") adj bit)	USPAT; US-PGPUB	2004/01/09 14:50
2	95	((((massively adj parallel adj processor) and ((single one (massively adj parallel adj processor)) adj bit)) and serial) and @ad <20000831) and (horizontal sideways\$1 ew we) and (vertical up down ns sn)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/09 14:59
3	122	((massively adj parallel adj processor) and ((single one 4380046.pn.) adj bit)) and serial) and @ad < "20000831"	USPAT; US-PGPUB	2004/01/09 15:01
-	5	5581773.pn. 5638533.pn. 5727229.pn. 5963746.pn. 6061367.pn.	USPAT; US-PGPUB	2002/08/02 11:05
-	1	4380046.pn.	USPAT; US-PGPUB	2002/08/01 19:09
-	844	mpp	USPAT; US-PGPUB	2002/08/01 19:10
-	299	massively adj parallel adj processor	USPAT; US-PGPUB	2002/08/01 19:11
-	154	(massively adj parallel adj processor) and ((single one 4380046.pn.) adj bit)	USPAT; US-PGPUB	2002/08/01 19:13
-	122	((massively adj parallel adj processor) and ((single one 4380046.pn.) adj bit)) and serial	USPAT; US-PGPUB	2002/08/01 20:07
-	347	massively adj parallel adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/02 11:06
-	156	(massively adj parallel adj processor) and ((single one (massively adj parallel adj processor)) adj bit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/02 11:08
-	120	((massively adj parallel adj processor) and ((single one (massively adj parallel adj processor)) adj bit)) and serial) and @ad <20000831	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/02 15:19
-	122	((massively adj parallel adj processor) and ((single one (massively adj parallel adj processor)) adj bit)) and serial	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/02 15:13
-	120	((massively adj parallel adj processor) and ((single one (massively adj parallel adj processor)) adj bit)) and serial) and @ad <20000831	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/02 15:14
-	120	((massively adj parallel adj processor) and ((single one ((massively adj parallel) array) adj processor)) adj bit)) and serial) and @ad <20000831	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/02 15:21
-	2552	array adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/02 15:21
-	347	massively adj.parallel adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/02 15:21
-	130	(array adj processor) and (massively adj parallel adj processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/08/02 15:22

-	2769	array adj processor) (massively adj parallel adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/02 15:22
-	2596	((array adj processor) (massively adj parallel adj processor)) and @ad <20000831	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/02 15:22
-	566	((array adj processor) (massively adj parallel adj processor)) and @ad <20000831) and ((single one "1") adj bit)	USPAT; US-PGPUB	2002/08/02 15:25
-	123	((array adj processor) (massively adj parallel adj processor)) and @ad <20000831) and ((single one "1") adj bit)) and (bit adj serial)	USPAT; US-PGPUB	2003/01/13 10:55
-	0	jp-9190421-A.did.	USPAT; US-PGPUB	2002/08/02 16:05
-	0	"9190421"	USPAT; US-PGPUB	2002/08/02 16:06
-	1	"9190421"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/02 16:06
-	0	"8963091"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/02 16:21
-	0	ep0463721a	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/02 16:21
-	0	("ep 463721").PN.	USPAT; US-PGPUB	2002/08/02 16:22
-	0	("ep 463721").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/02 16:24
-	0	eastty-p.in.	USPAT; US-PGPUB	2002/08/02 16:25
-	4	eastty-p.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/02 16:27
-	0	Data adj communication adj system adj of adj computer	EPO	2002/08/02 16:28
-	0	Data adj communication adj system adj of adj computer	JPO	2002/08/02 16:28
-	3	(horizontally and vertically and PE).ab.	JPO	2002/08/02 16:31
-	0	("jp 9190421").PN.	USPAT; US-PGPUB	2002/08/02 16:30
-	0	("jp 9190421 a").PN.	USPAT; US-PGPUB	2002/08/02 16:30
-	12143	(horizontally and vertically).ab.	JPO	2002/08/02 16:31
-	94	(horizontally and vertically and ports).ab.	JPO	2002/08/02 16:33
-	223	(horizontally and vertically and ports).ab.	JPO; DERWENT	2002/08/02 16:41
-	5	chen-k.in.	EPO	2002/08/02 16:42
-	9694	chen.in.	EPO	2002/08/02 16:42
-	690	svensson.in.	EPO	2002/08/02 16:48
-	6941	fujioka.in.	JPO	2002/08/02 16:48
-	34	fujioka-yoichi.in.	JPO	2002/08/02 16:55
-	1	kirsch-graham.in.	EPO; DERWENT	2002/08/02 16:55
-	1897	(multiplexer and tri-state and register and memory and (processor processing) and bus)	USPAT; US-PGPUB	2002/08/04 18:24

-	1832	((multiplexer and tri-state and register and memory and (processor processing) and bus)) and @ad < "20000831"	USPAT; US-PGPUB	2002/08/04 19:10
-	70	((multiplexer and tri-state and register and memory and (processor processing) and bus)) and @ad < "20000831" and (parallel adj processor)	USPAT; US-PGPUB	2002/08/04 19:10
-	3008	(711/5,101,104,105,154,165,170,173).CCLS.	USPAT; US-PGPUB	2002/08/05 11:11
-	596	(712/2,6,10,11,14,15).CCLS.	USPAT; US-PGPUB	2002/08/05 11:11
-	3590	((711/5,101,104,105,154,165,170,173).CCLS.)	USPAT; US-PGPUB	2002/08/05 11:12
-	3223	((711/5,101,104,105,154,165,170,173).CCLS.)	USPAT; US-PGPUB	2002/08/05 11:12
-	205	((712/2,6,10,11,14,15).CCLS.)) and @ad < "20000831"	USPAT; US-PGPUB	2002/08/05 11:12
-	116	(((((711/5,101,104,105,154,165,170,173).CCLS.)) and @ad < "20000831" and (parallel adj processor)	USPAT; US-PGPUB	2002/08/05 11:13
-	49	(((((711/5,101,104,105,154,165,170,173).CCLS.)) and @ad < "20000831" and (parallel adj processor) and ((single one "1") adj bit)) and (bit adj serial)) and (vertical and horizontal)	USPAT; US-PGPUB	2003/01/13 11:06
-	362	(((((array adj processor) (massively adj parallel adj processor)) and @ad <20000831) and ((single one "1") adj bit)) and serial	USPAT; US-PGPUB	2003/01/14 16:31
-	46	(((((array adj processor) (massively adj parallel adj processor)) and @ad <20000831) and ((single one "1") adj bit)) and serial) and ((processor (processing adj element) cpu) with memory with (('same' single) near (chip IC)))	USPAT; US-PGPUB	2003/01/14 16:34